

FIG.2A

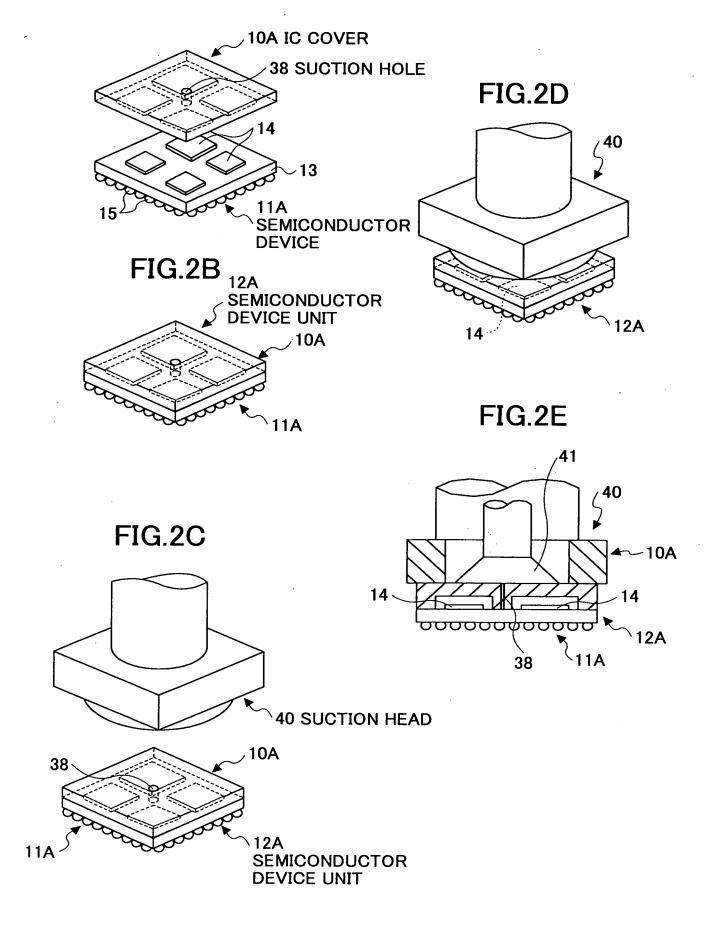


FIG.3

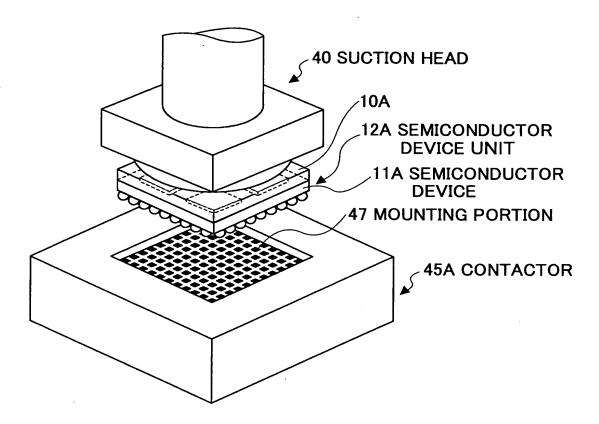
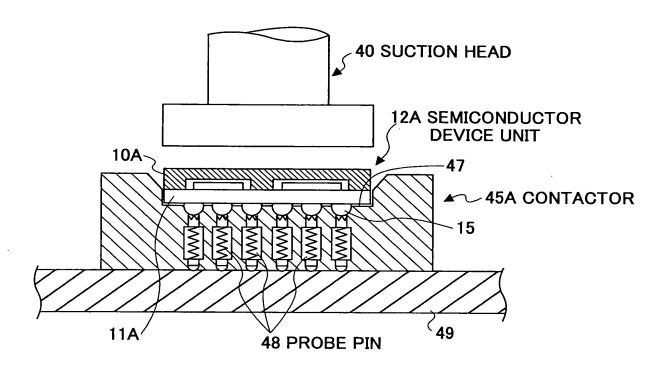


FIG.4



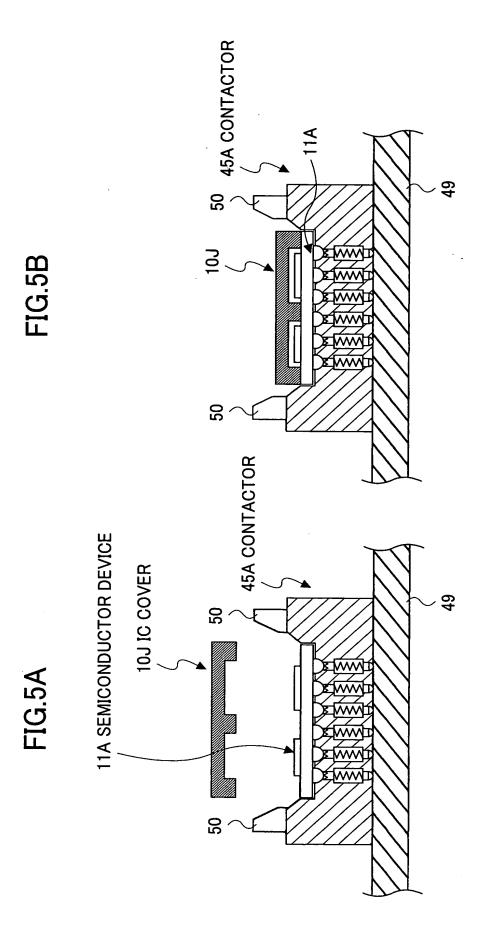
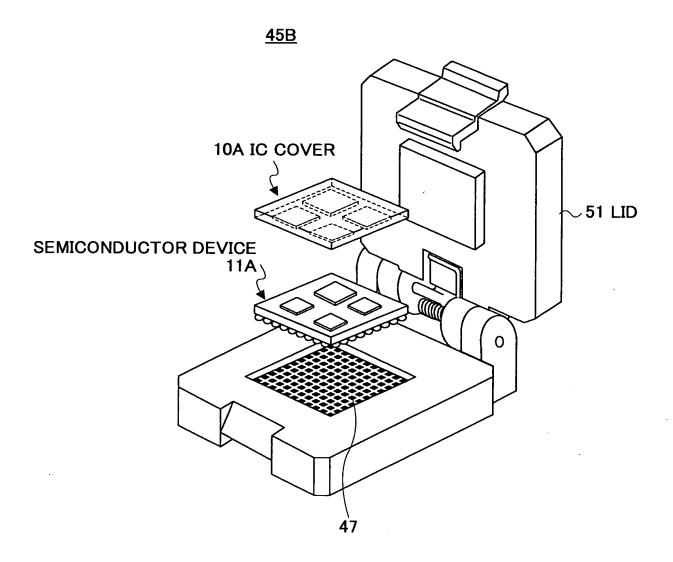
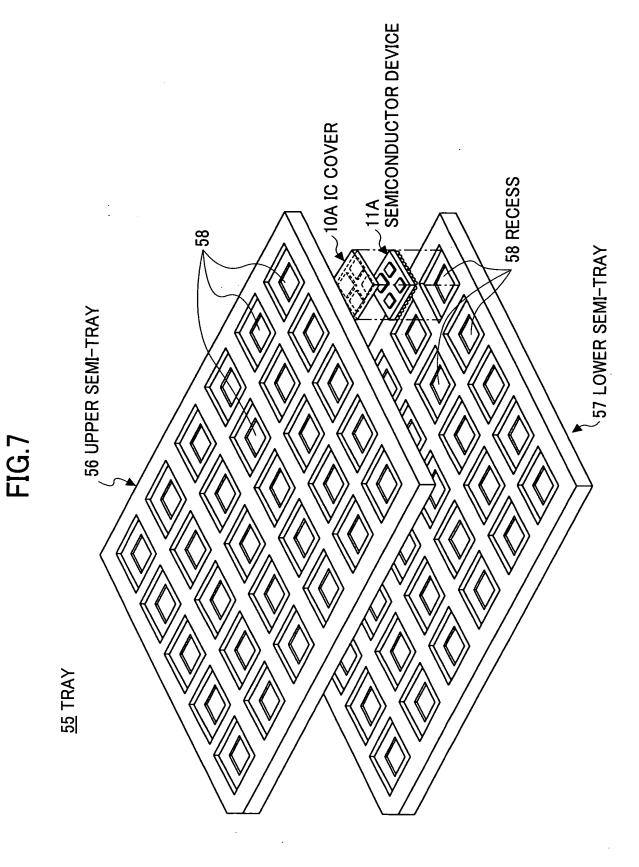
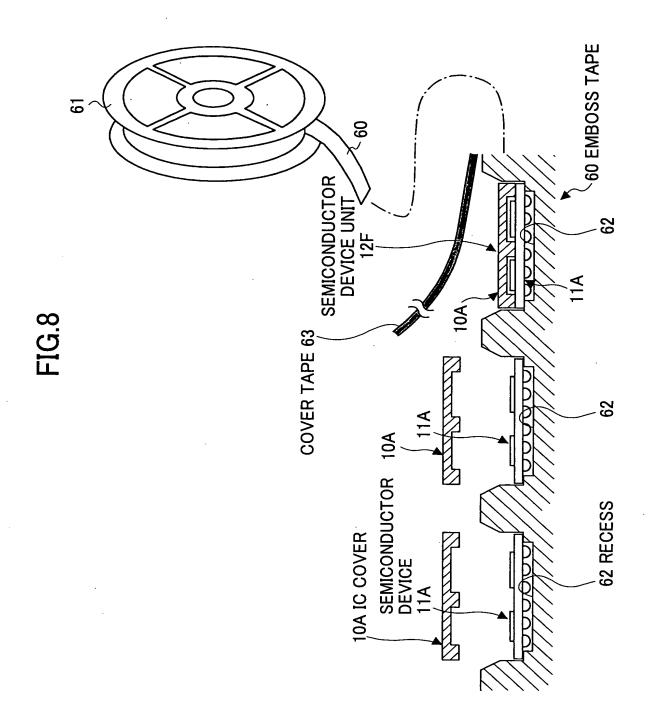
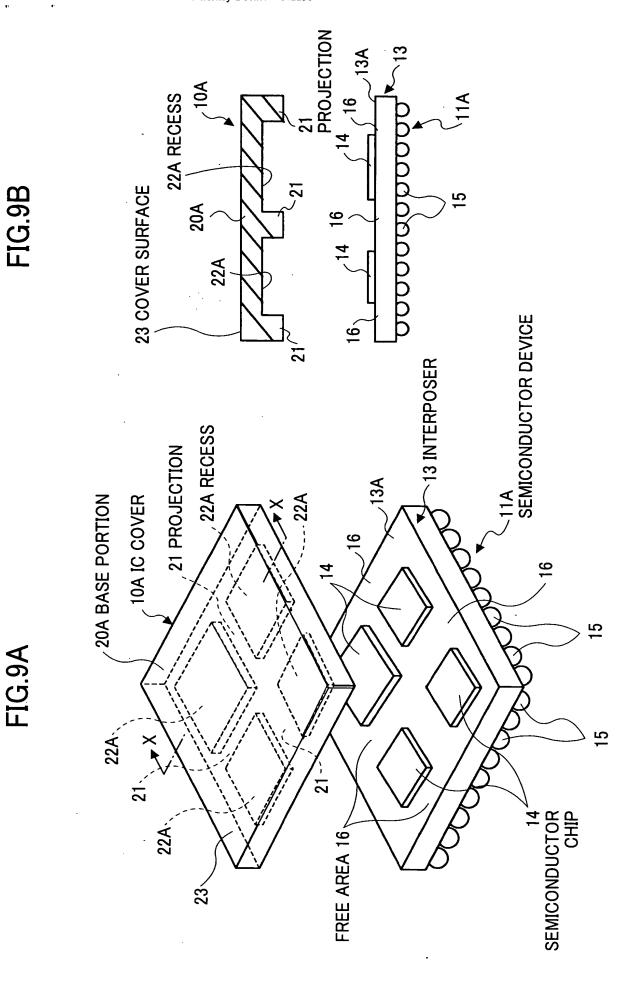


FIG.6









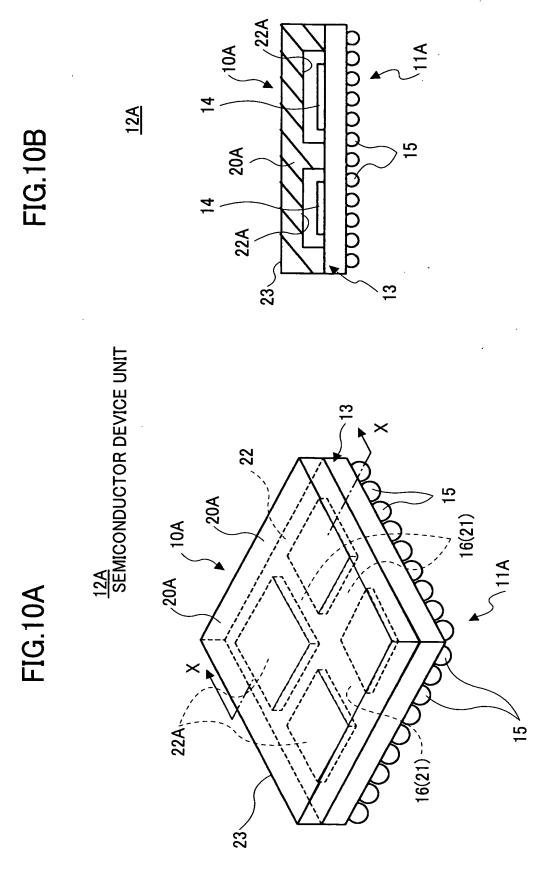


FIG.11

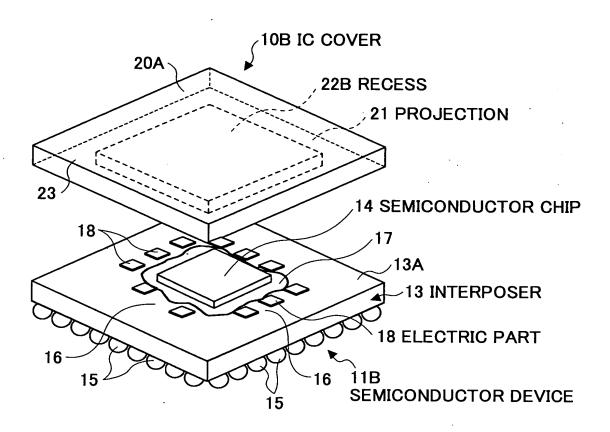
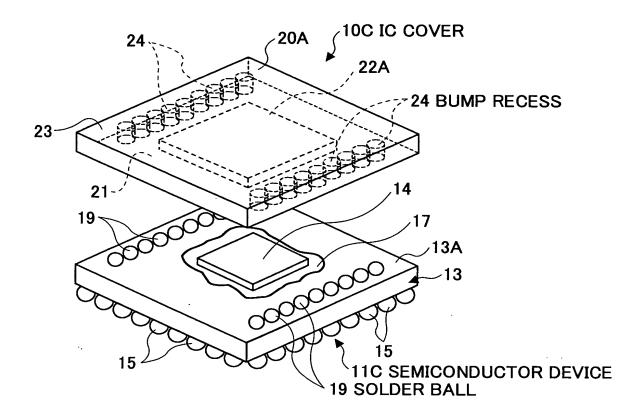
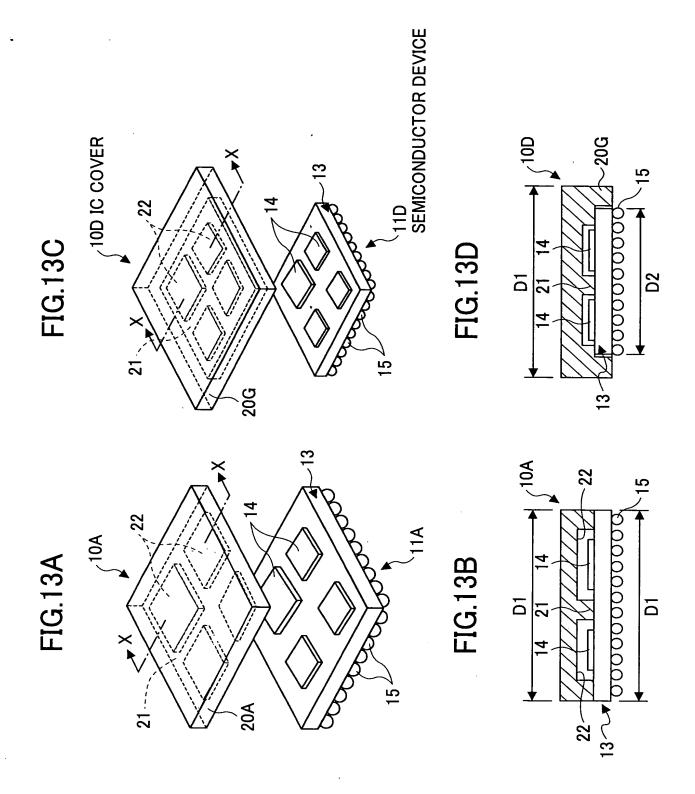


FIG.12

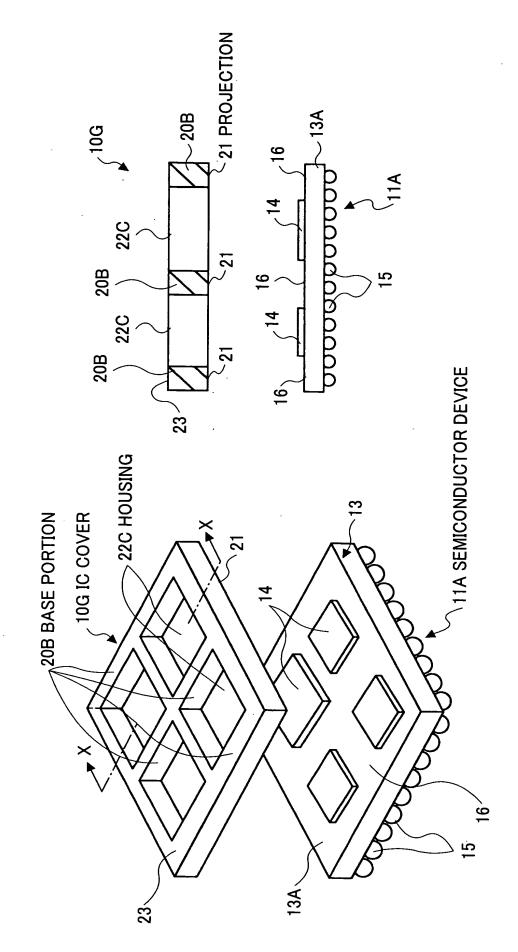




WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP
Title: Method of Semiconductor Device Protection,...
Inventor: Kazuhiro TASHIRO et al.
Attorney Docket: 042236

FIG.14B

FIG.14A



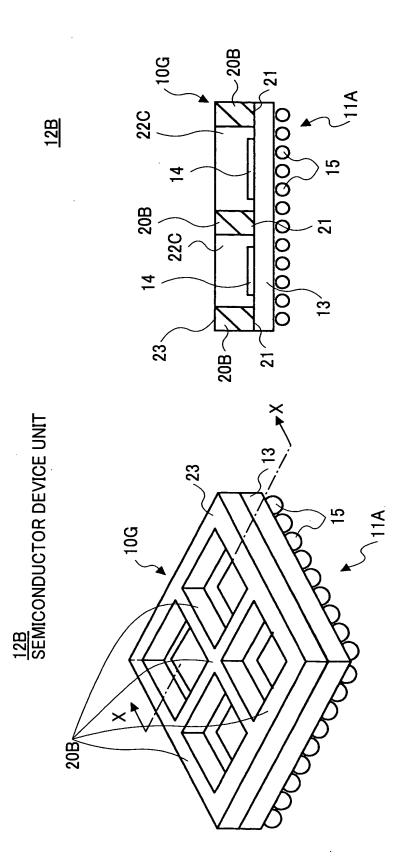


FIG.16B

FIG.16A

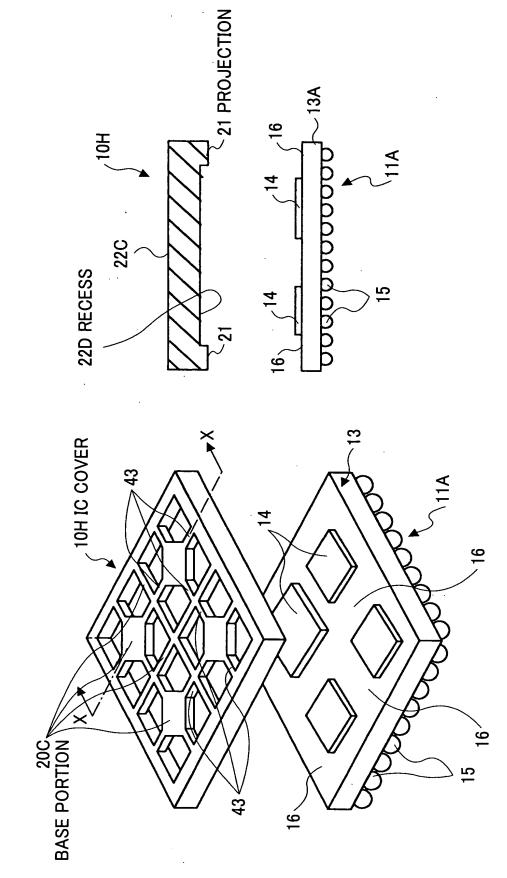
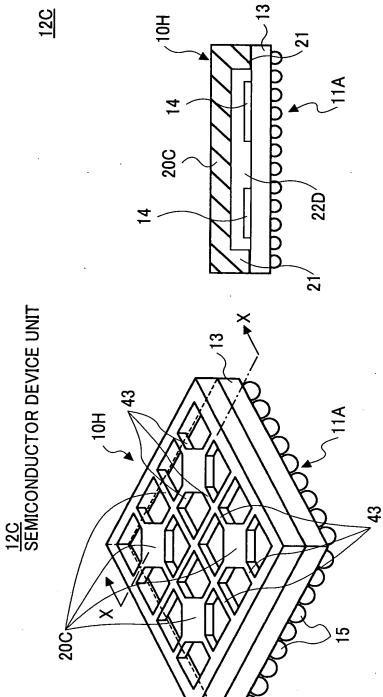


FIG.17A



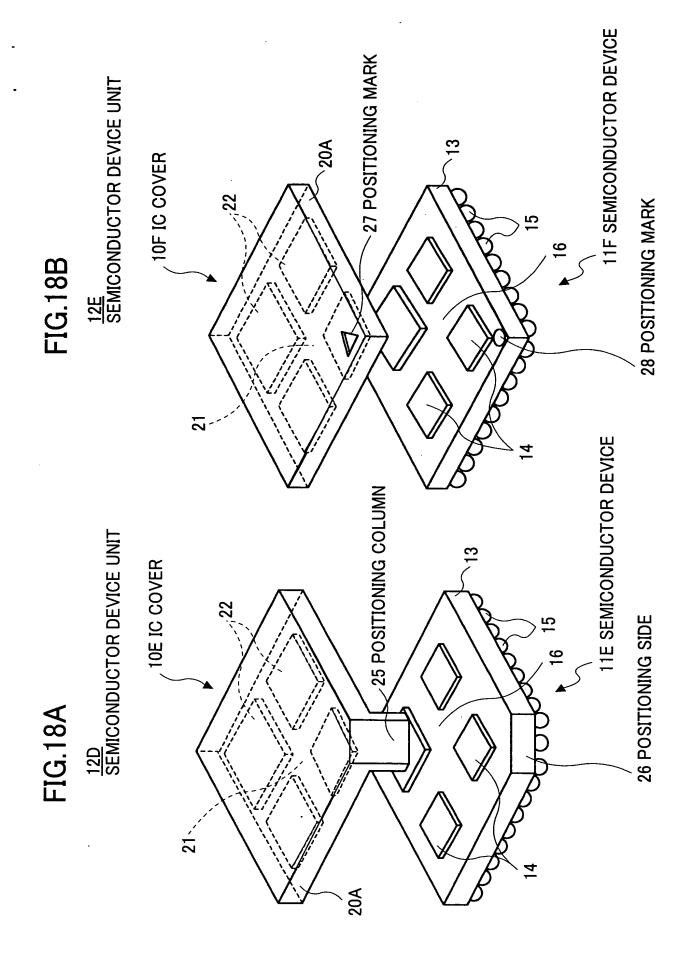


FIG.19

12F SEMICONDUCTOR DEVICE UNIT

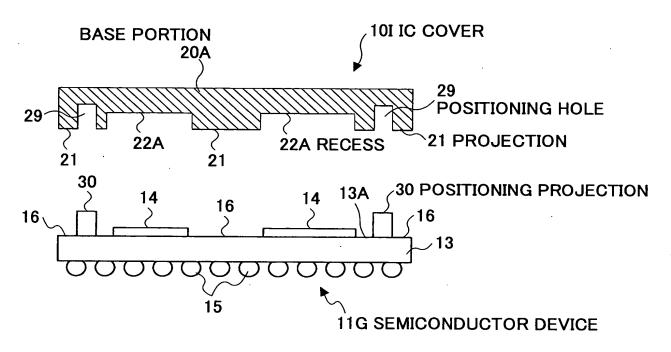


FIG.20

12G SEMICONDUCTOR DEVICE UNIT

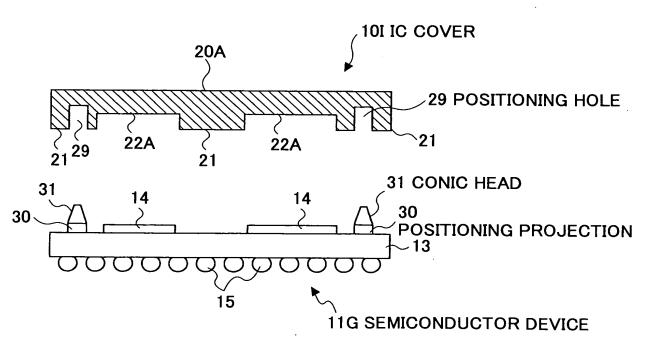


FIG.21

12H SEMICONDUCTOR DEVICE UNIT

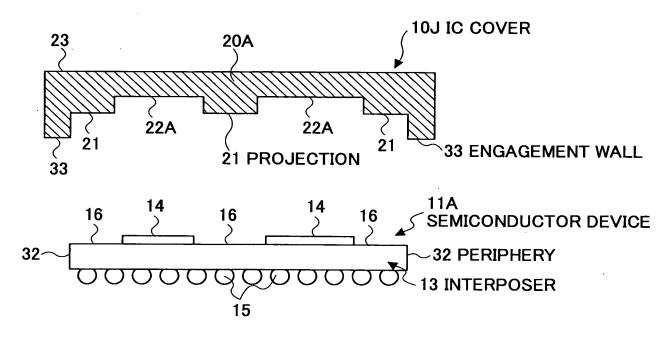


FIG.22

12I SEMICONDUCTOR DEVICE UNIT

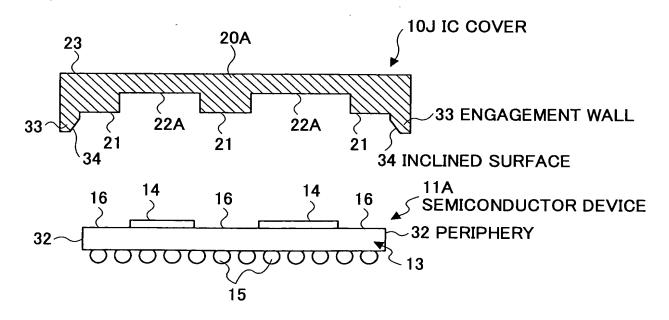
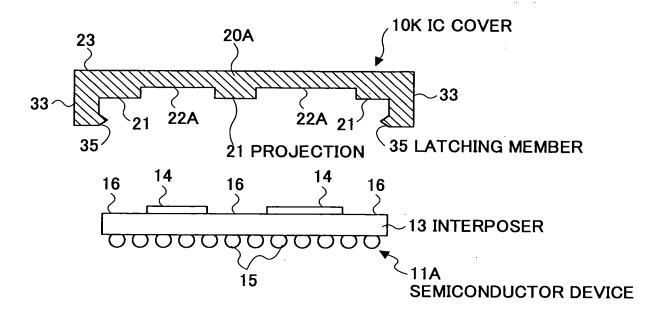
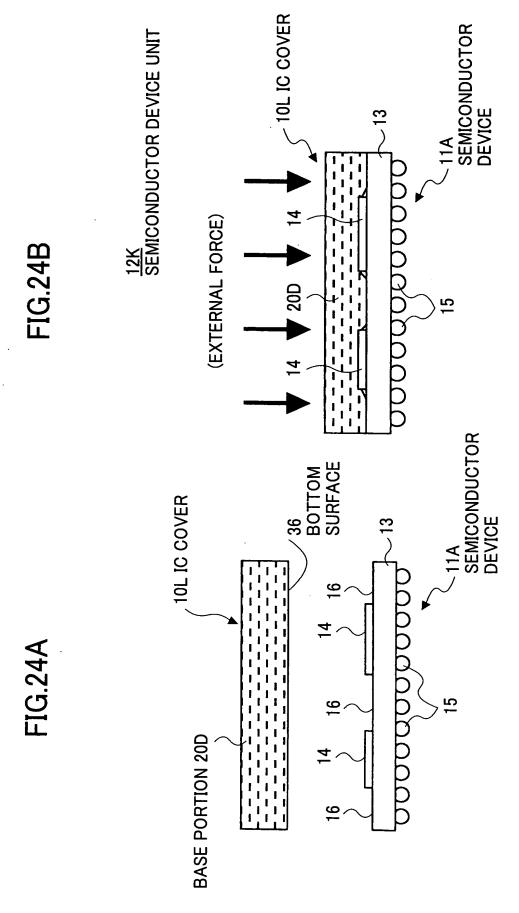


FIG.23

12J SEMICONDUCTOR DEVICE UNIT





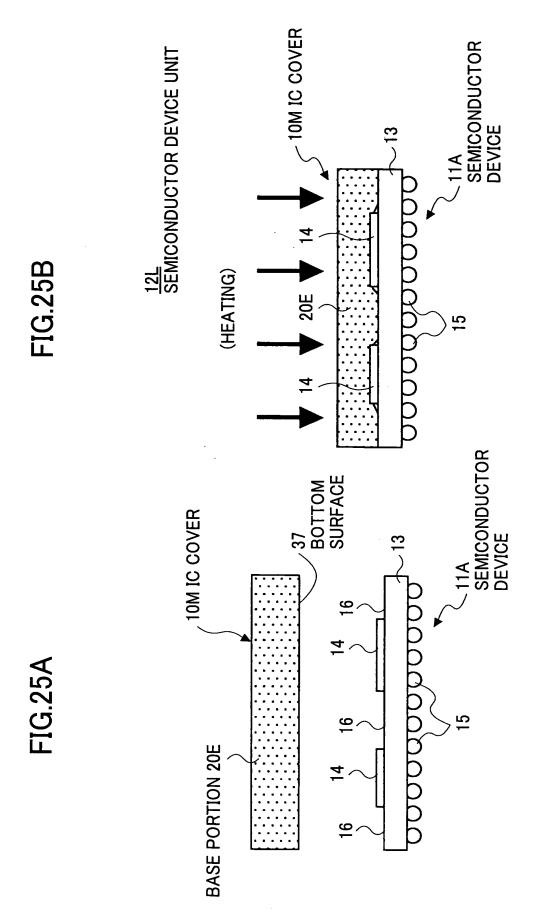


FIG.26

